

Patent

Attorney Docket No.: Intel 2207/10121
Assignee: Intel Corporation

Response Under 37 C.F.R. 1.116
Expedited Procedure
Examining Group 2183

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT : Sailesh KOTTAPALLI et al.
SERIAL NO. : 09/751,762
FILED : December 29, 2000
FOR : METHOD FOR CONVERTING PIPELINE
STALLS CAUSED BY INSTRUCTIONS WITH
LONG LATENCY MEMORY ACCESSES TO
PIPELINE FLUSHES IN A MULTITHREADED
PROCESSOR
GROUP ART UNIT : 2183
EXAMINER : Daniel H. PAN

M/S: AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO FINAL OFFICE ACTION

Dear Sir:

The following amendments and remarks below are respectfully submitted in
response to the Office Action dated September 24, 2007.

Listing of the Claims begin on page 2 of this response.

Remarks/ Arguments begin on page 7 of this response.